

Thapar Institute of Engineering  
& Technology, Patiala

presents

WEBINAR  
series on  
**VLSI  
DESIGN**

3rd  
Webinar

Topic:  
From Campus to Corporate

Speaker



**Manoj Dadhich**  
Engineering Director  
Intel Corporation, Bangalore, India

Department of Electronics  
and Communication Engineering  
through  
VLSI Chips to System Design  
Group

Contact : Dr. Anil Singh, ECED  
anils.rawat@thapar.edu

Date: 14th Aug., 2021  
(Saturday)

Time: 11:00AM-12:00PM