

Thapar Institute of Engineering and Technology, Patiala

Deemed to be University

Patiala - 211015, Punjab

Visvesvaraya PhD Scheme: Phase-II (Domain: ESDM/IT/ITES)

TIET Patiala invites applications for admission to the Ph.D. programs for admission in Session July 2024 (Through regular admission process) under the Visvesvaraya Ph.D. Scheme: Phase-II. A total of 2 vacancies (1 Full-time and 1 Part-time) are available (in ESDM*/IT/ITES* including C2S* Program) under Visvesvaraya Ph.D. scheme for Electronics and IT for July 2024 Session. Candidates admitted under Visvesvaraya Ph.D. scheme will be governed by the guidelines issued by the Ministry of Electronics and IT (MEITY), Govt. of India.

PhD students who take admission to TIET, Patiala during session July 2024-25 through regular institute process in the above domains will only be considered.

***ESDM** - Electronics System Design and Manufacturing, **ITES** - Information Technology Enabled Services, **C2S** - Chips to Start-up

Salient features of the Visvesvaraya Scheme (Full-Time PhD Candidates)

- The candidates would be enrolled at the supported institution (IIIT Allahabad) under the scheme as per the procedural norms and 'Terms & Conditions' of supported institution as well as the Terms & Condition of the Visvesvaraya Ph.D. scheme: Phase - II.
- Financial support provided to each Full-Time PhD candidate is as mentioned below.
 - **Fellowship:** Rs 38,750/- per month (I & II year); Rs 43,750/- (III to V year).
 - **Research Contingency Grant Support:** An amount of Rs. 1,20,000/Year/Full-time PhD candidate for support duration of Ph.D. candidate.
 - Reimbursement of Rent for those Full-Time Ph.D. candidates who are not provided accommodation inside TIET Campus. The ceiling for Reimbursement

of Rent would be 8% of the fellowship amount as per the Government of India norms for class Z city.

- **Support for attending International Conference:** Support up to Rs.1.5 Lakhs/Full-time PhD candidate.
- **Visit to Labs abroad (From III Year):** Only the selected candidates fulfilling eligibility criteria based on the guidelines to be provided in due course will be supported.

Salient features of the Visvesvaraya Scheme (Part-Time PhD Candidates)

- Part-time PhD seats is provisioned for professionals working at a reputed place/organization
- The selected candidates should not be availing any PhD Fellowship/scholarship from any other scheme of Central or State Government.
- Part-time candidates shall be provided a one-time incentive of Rs. 3 Lakh on successful completion of the PhD degree.

Further guidelines if any will be provided subsequently.

For more details regarding Visvesvaraya Ph.D. scheme, interested candidates may explore the webpage: <http://phd.digitalindiacorporation.in/>

Last date for receiving applications at TIET Patiala (Online portal) is July 15, 2024. For more details regarding the procedure for admissions to Ph.D. at TIET Patiala, interested candidates may refer the webpage:

[Thapar Institute – B. Tech Admission 2024, M. Tech Admission 2024, and PHD Admission 2024](#)

Nodal Officer,

Visvesvaraya PhD Scheme for Electronics & IT- Phase II
TIET, Patiala

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