

Thapar Institute of Engineering
& Technology, Patiala

presents

WEBINAR
series on
**VLSI
DESIGN**

4th
Webinar

Topic:
Semiconductor Memories

Speaker



Naman Rastogi
Staff Engineer
Western Digital, Bengaluru, India

Department of Electronics
and Communication Engineering
through
VLSI Chips to System Design
Group

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**Date: 4th Sept., 2021
(Saturday)**

Time: 11:00AM-12:00PM