

Thapar Institute of Engineering and Technology, Patiala

Electrical and Instrumentation Engineering Department

**ADVERTISEMENT FOR JRF VACANCY IN DST SPONSORED PROJECT UNDER THE
DEVISE DEVELOPMENT PROGRAMME**

Sanctioned File No: File No: DST/TDT/DDP-06/2021

Project title: Designing of AC and DC Net Meter for Electric Vehicle Application

Position: JRF (2) (Expertise in Microcontroller/ Web development/Communications)

Duration: Two and half years

Stipend: As per DST norms

Qualifications:

Essential: Master's degree in electrical /instrumentation/ control systems/ Electronics/Computer Science Engineering or related with minimum of 60% marks or 7.0 CGPA.

Specialization: Microcontroller, Web development, Instrumentation or Communications

Desirable:

- I. Working knowledge of Web development/App development/Data base management for JRF 1. For JRF 2, working knowledge of different microcontroller, PCB design and ready to work on any electronics hardware.
- II. Candidates with NET/GATE and research experience in the relevant field would be given preference.

Last date of application: 22th March, 2021.

1. Interested candidates may send their typed CV* through email (with subject as "JRF Vacancy for DST-Devise development Project") to the PI (mukesh.singh@thapar.edu) before last date.
2. No application would be considered after the due date.
3. Shortlisted candidates will be informed about the interview date through email only.
4. No TA/DA will be given to the candidates called for the interview.
5. The candidate selected for JRF may also get enrolled for Ph.D. degree simultaneously as per the Institute norms.

Dr. Mukesh Singh (PI)

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***Note:** The CV should include candidate's name, date of birth, address, mobile, e-mail, qualifications (with subject, year of passing, %age/cgpa), achievements, list of publications (if any), research experience (if any), name of two referees and hardware experience if any (200 words).