

**Department of Electrical and Instrumentation Engineering
Thapar Institute of Engineering and Technology
Patiala (Punjab)-147001**

Notice

Applications are invited for one post of JRF under the CSIR funded Project detail mentioned below:

Title of Project	DFOG20: Design and Development of an Efficient Defogging Framework for Road Safety	
File Number	22(7611)/19/EMR-II, Dated 01/07/2019	
Tenure of The Project	36 Months from date of commencement of the project	
Number of Post	One JRF	
Name of the Post with Salary	Essential Qualification	Upper Age Limit
Junior Research Fellow (JRF) @Rs. 31,000/- pm Plus HRA/MA	Possessing M.Tech. Degree in Computer Science and Engineering/ Instrumentation Engineering/Instrumentation and Control engineering /Electronic and Communication Engineering with 55% marks and passing NET/GATE test in Computer Science and Engineering/ Electrical and Instrumentation/	28 Years
Desirable	Good knowledge of MATLAB/Python	
Age Relaxation	Upper age limit is relaxable upto 5 years for SC/ST/OBC/Women and Physically handicapped candidates	

Eligible and interested candidates are requested to send their **Curriculum Vitae along with self attested copies of supporting documents/certificates (with contact number and email id) by email or post to the address given below by 15th Dec., 2019**. Candidates recruited may register for pursuing Ph.D. programme in Department of Electrical and Instrumentation, T.I.E.T, Patiala with joint collaboration of NIT, Hamirpur as per the Institute rule. Short listed candidates will be called for the interview. The date and time for the interview will be informed to the candidates only through e-mail or display on our Institute website www.thapar.edu. No TA/DA will be paid for attending the interview.

Also fill up the details, in the google form given below

<https://forms.gle/BbUV1ZuWhoBmGJW79>

Dr. Sunil Singla
Principal Investigator, CSIR (Project)
Email: ssingla@thapar.edu
Contact No.: 9463909829

1.	Name (in block letters) :						Affix a Passport size Photograph
2.	Father's Name :						
3.	Address for correspondence: (complete Address with pincode)		<hr/> <hr/> <hr/> Mobile: _____ E-mail: _____				
4.	Category (Gen./OBC/SC/ST):						
5.	Date of Birth : (dd/mm/yyyy)						
6.	Educational Qualification:		(in following given order , starting from High School onwards)				
	S.No	Degree	Board/University	Year	Percentage Marks/ CGPA	Division	Subjects
7.	Details of GATE /NET Qualification (if qualified)		Qualification Year :		Validity:		
			Discipline:		Rank:		
			Score:		Marks obtained:		
8.	Experience (Teaching/Industry/Research)		(Starting from latest)				
	S.No.	Organization	Duration	Salary	Nature of Work		

9.	Title of M.Phil./M.Sc./Ph.D. Dissertation/Thesis	
10.	Professional Skills (Give details of hands on experience on simulation software and knowledge of hardware/experimental skills related to project)	
11.	Details of Research Publications (if any)	Please attach separate sheet
12.	Any other relevant information:	
13.	References (Two) (Name of two referees who are familiar with your academic and research background)	

I hereby declare that the above information is correct to the best of my knowledge and belief. I understand that my candidature will be cancelled if any information is found incorrect at any given period of time.

Signature with Name and Date