

**THAPAR INSTITUTE OF ENGINEERING AND TECHNOLOGY, PATIALA - 147004**

(Established u/s 3 of the UGC act, 1956 vide Notification No. F-9-12/84-u.3 of Gol)

**ONLINE INTERVIEW**

Walk in Interview for the following post in the project "Special Manpower Development Program for Chips to System Design (SMDP-C2SD)" sponsored by MeitY (Govt. of India), New Delhi (on temporary basis) will be held on **18<sup>th</sup> September, 2020 at 10 a.m.** via online mode (Zoom link).

**Project Associate**

VACANCY: 01

SALARY: (22,000/- - 26,000/- per month consolidated) Depending upon experience.

QUALIFICATIONS: ME/ M.Tech in VLSI Design / Microelectronics / or equivalent.

DESIRABLE: Willing to do research related to chip design and test.

**JOB REQUIREMENTS**

- Carrying out R&D work to support the execution of VLSI Projects and related activities.
- Physical Design (From RTL to Layout).
- Hardware Chip Testing.

All the appointments will be done on temporary basis till the completion of project. Interested candidates must have scanned copies of original certificates, a copy of resume mentioning educational qualifications, DOB, professional experience (if any), two references, contact details including email and phone numbers, etc.

**An advance copy of resume may be forward to the Chief Investigator at the email address given below. Zoom link will be provided to the candidates via email.**

**Dr. Alpana Agarwal**

**Chief Investigator, SMDP-C2SD Project**

**ECED, Thapar Institute of Engineering and Technology, PATIALA-147004**

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