THAPAR INSTITUTE OF ENGINEERING AND TECHNOLOGY, PATIALA - 147004

(Established u/s 3 of the UGC act, 1956 vide Notification No. F-9-12/84-u.3 of GoI)

ONLINE INTERVIEW

Online Interview for the following post in the project "Special Manpower Development Program for Chips to System Design (SMDP-C2SD)" sponsored by MeitY (Govt. of India), New Delhi (on temporary basis) will be held on **9th April, 2021** at **11.30 am** via **Online mode (Zoom Link)**.

Lab Engineer

Vacancy: 01
Salary: (30,000/- to 45,000/- per month consolidated) depending upon experience.
QUALIFICATIONS: B.E. (Electronics & Communication) / or equivalent.
Desirable: M.E. / M.Tech in VLSI Design / ECE / Microelectronics / or equivalent.
          PG qualified candidates may be given a higher start.

Job Requirements
- PCB design and chip test to support the execution of VLSI projects and related activities.
- Maintenance of Lab and Computing Machines.
- Up gradation and Installation of Operating Systems and VLSI Design Tools.
- Preparation of Project Reports, UCs, Project documentations etc.
- Any other related work assigned time to time.

All the appointments will be done on temporary basis till the completion of project. Interested candidates must have scanned copies of original certificates, a copy of resume mentioning educational qualifications, DOB, professional experience (if any), two references, contact details including email and phone numbers, etc.

Zoom link will be provided to the candidates via email.

An advance copy of resume may kindly be forward to the Chief Investigator at the email address alpana@thapar.edu

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