

Department of Electronics and Communication Engineering (ECED)

THAPAR UNIVERSITY, PATIALA

Thapar Technology Campus, Patiala – 147004 (INDIA)

Advertisement for the post of Junior Research Fellow

Applications are invited from motivated, dedicated and eligible Indian nationals for the post of **Junior Research Fellow (JRF) (on contract)** in the research project entitled “**Low-Complexity Power-Efficient Reconfigurable Implementation of Fractional Order Filters for Weak ECG Nonstationary Biomedical Signal Processing Applications**” under Department of Science and Technology (DST) vide sanction order no. SB/S3/EECE/0149/2016 dated September 27, 2016 sponsored by Science and Engineering Research Board (SERB).

The **only /pdf version** of duly completed application on prescribed format must reach to: **sanjay.kumar@thapar.edu** *on or before* October 14, 2016.

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|----------------------------------|---|
| Number of Position: | 1 (One) |
| Essential Qualifications: | Post-Graduate (M.E./M.Tech.) in Electronics & Communication Engineering/ Instrumentation Engineering or equivalent, with specialization in Signal Processing and Embedded System Design, with marks not below 75% and should possess basic under-graduate B.E./B.Tech. degree in Electronics & Communication Engineering with marks not below 75% and a valid GATE score. |
| Desirable Experience: | <p>(i) Good research potential in the related area (in the form of demonstrative research projects/ thesis) along with relevant credentials in the form of Science Citation Indexed Impact Factor Journal publications, Patents, etc.</p> <p>(ii) Good experience with languages such as LabVIEW / MATLAB / Python / C / C++ ; prior knowledge of VHDL / Verilog is required ; should have the capability of various Signal Processing, Circuits and Embedded Systems (especially FPGAs), Machine Learning, Estimation Theory, Evolutionary Algorithms concepts.</p> <p>NOTE: Please <u>do not apply</u> if you are not well-versed, proficient and comfortable with the computational programming languages.</p> <p>(iii) Excellent interpersonal and communication skills (both oral and written).</p> |
| Fellowship: | The fellowship amount will be as per DST–SERB norms. |
| Other Benefits: | The selected candidate has the provision to register for Ph.D. program of the University, as per the prevailing University guidelines. |
| Age Limit: | Below 30 years on the last date of application (Relaxation for SC/ST/OBC candidate as per Government of India norms). |
| Tenure of Appointment: | Appointment will be made on contract basis for a period of one year initially, which may be extended further depending on the performance |

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| | evaluation on yearly basis and on the approval of the sanctioning agency DST–SERB till the end of the project. The appointment is for a time bound project and the candidate is required to work mainly for the successful completion of the project. |
| Last date for application submission | October 14, 2016 |
| Tentative Interview Date | October 18, 2016 (will be notified via website or email) |

NOTE:

- (1) Only candidates having excellent academic record and a keen interest in research are highly encouraged to apply.
- (2) The applicant will be responsible for the authenticity of information, other documents and photograph submitted.
- (3) Mere possessing the prescribed qualification does not ensure that the candidate would be called for interview. The candidates will be shortlisted on the basis of merit and need of the project.
- (4) Shortlisted candidates will be informed through website or email.
- (5) Shortlisted candidates have to present themselves personally for the interview on the scheduled interview date and time, with updated CV and original and attested xerox copies of marks sheets/ certificates in support of their academic qualifications.
- (6) Applicants in employment (private, government or any other organization) are required to submit a “**No Objection Certificate**” from the employer at the time of interview.
- (7) Complete information with regard to GATE score such as: year of passing, discipline, score, marks, All India Rank and the number of candidates appeared should be mentioned in the CV. Incomplete applications will be rejected.
- (8) No TA/DA will be paid for appearing in the interview. Candidates should make their own arrangements for stay.

Contact Person: Dr. SANJAY KUMAR, Principal Investigator

Designation: Assistant Professor

Department: Electronics and Communication Engineering

Email ID: sanjay.kumar@thapar.edu

Format for JRF Application

1) Application for the post of:

2) Name of the Candidate in full (in capital letters):

3) Date of Birth:

4) Nationality:

5) Email Address (important to fill):

6) Mailing Postal Address:

Photograph here

7) Educational qualifications (starting from High school onwards)

| S. No. | Name of Exam passed | Subjects/ Branch/ Specializations | Name of the University/ Board | Month & Year of exam passed | Marks secured | Percentage of Marks secured/ CGPA |
|--------|----------------------|-----------------------------------|-------------------------------|-----------------------------|---------------|-----------------------------------|
| [1] | X | | | | | |
| [2] | XII | | | | | |
| [3] | Graduation | | | | | |
| [4] | Post Graduation | | | | | |
| [5] | Any other additional | | | | | |

8) Particulars of GATE examination:

(i) Year of passing:

(ii) Discipline:

(iii) Score (percentile):

(iv) All India Rank:

(v) The number of candidates appeared:

9) Previous experience:

(Furnish details such as name of the post/fellowship, period worked, name of the organization/institution etc.)

10) Present position:

(a) If you are studying, mention course/subject details:

(b) Details of fellowship/post:

(c) Name of the institution/organization in which you are studying/working:

11) M.E./M.Tech. Thesis/Project title, with details of software language used:

12) Number of Research papers published (only SCI):

13) Any other hardware projects handled:

Date:

Signature of the Candidate