

UEIXXX: FUNDAMENTALS OF MICROPROCESSORS AND MICROCONTROLLERS

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| L | T | P | Cr |
| 3 | 1 | 2 | 4.5 |

Course Objectives: To make the students able to understand microprocessors and microcontroller and their applications.

INTEL 8086 Microprocessor: Pin Functions, Architecture, Characteristics and Basic Features of Family, Segmented Memory, Addressing Modes, Instruction Set, Data Transfer Instructions, Arithmetic, Logical, Shift and Rotate Instructions, String Instructions, Flag Control Instructions, Transfer of Control Instructions, Processor Control Instructions, Programming Examples, Interrupt Structures, Multitasking and Multiprogramming, MIN/MAX Modes of 8086, Co-processors 8087 and 8089.

Introduction to 8051 Microcontroller : 8051-architecture and pin diagram, Registers, Timers Counters, Flags, Special Function Registers, Addressing Modes, Data types, instructions and programming, Single –bit operations, Timer and Counter programming, Interrupts programming, Serial communication, Memory accessing and their simple programming applications.

Hardware interfacing: I/O Port programming, Bit manipulation, Interfacing to a LED, LCD, Keyboard, ADC, DAC, Stepper Motors and sensors.

Laboratory work: Introduction to INTEL kit, Programming examples of 8086, Interfacing using 8086 kits, ADC, DAC, 8253, Microprocessor based project, Programming and Application development around 8051, Interfacing to LED, LCD, Keyboard, ADC, DAC, Stepper Motors and sensors etc.

Course Learning Outcome (CLO):

After the successful completion of the course the students will be able to:

1. demonstrate the concept of microprocessor and to be able to design a microprocessor based system to get desired results.
2. use 8086 microprocessor in advanced applications, which will give them a good platform to work further.
3. graduates will be able to update with current trends through self-study and show genuine need to learn on continuous basis.
4. students will be able to use hardware interfacing of 8051 to develop solutions of real world electrical problems.

Text Books:

1. Hall, D.V., *Microprocessor- Interfacing Programming and Hardware*, Tata McGraw–Hill (1997).
2. Ayala, K.J., *The 8051 Microcontroller Architecture, Programming and applications*, Penram International Publishing (India) Pvt. Ltd. (2007).
3. Mazidi, M.A., *The 8051 Microcontroller and Embedded System*, Pearson Education (2008).

Reference Books:

1. Brey, B.B., *The INTEL Microprocessors*, Prentice–Hall of India Private Limited (2002).
2. Liu, Y. C. and Gibson, G.A., *Microcomputer Systems: The 8086/8088 Family– Architecture, Programming and Design*, Prentice–Hall of India Private Limited (2007).
3. Uffenbeck, J., *The 8086/ 8088 Family*, Prentice–Hall of India Private Limited (1994).
4. Predko, M., *Customizing The 8051 Microcontroller*, Tata McGraw–Hill (2002).

Evaluation Scheme:

| S.NO. | Evaluation Elements | Weightage |
|-------|--|-----------|
| 1 | MST | 25 |
| 2 | EST | 35 |
| 3 | Sessional (May include Assignments//Quizzes/Lab Evaluations) | 40 |