

Course Syllabi: UEE693: Capstone Project Start (L : T : P :: 0 : 0 : 2)

1. Course number and name: UEE693: Capstone Project

2. Specific course information

Course Objective: To facilitate the students learn and apply an engineering design process in electrical engineering, including project resource management. As a part of a team, the students will make a project, that emphasizes, hands-on experience, and integrates analytical and design skills. The idea is to provide an opportunity to the students to apply what they have learned throughout the course of graduate program by undertaking a specific problem.

Course Description: Capstone Project is increasingly interdisciplinary, and requires students to function on multidisciplinary teams. It is the process of devising a system, component or process to meet desired needs. It is a decision-making process (often iterative), in which the basic sciences, mathematics, and the engineering sciences are applied to convert resources optimally to meet these stated needs. It typically includes both analysis and synthesis performed in an iterative cycle. Thus, students should experience some iterative design in the curriculum. As part of their design experience, students have an opportunity to define a problem, determine the problem scope and to list design objectives. The project must also demonstrate that students have adequate exposure to design, as defined, in engineering contexts. Engineering standards and realistic constraints are critical in engineering design. The program must clearly demonstrate where standards and constraints are taught and how they are integrated into the design component of the project. Each group will have 4-5 students. Each group should select their team leader and maintain daily diary. Each Group will work under mentorship of a Faculty supervisor. Each group must meet the assigned supervisor (2hrs slot/week) till the end of the semester (record of attendance will be maintained), as per the time slot which will be provided to them by the respective supervisor. This is mandatory requirement for the fulfilment of the attendance as well as the successful completion of the project. The faculty supervisor of the project will continuously assess the progress of the works of the assigned groups. Some part of the analysis and design of the system will be done in the first section of project in semester VI. The second section would comprise of completion of the project in semester VII in which each team will have to submit a detailed report of the project along with a poster.

3. Specific goals for the course

After the completion of the course, the students will be able:

- To identify design goals and analyze possible approaches to meet given specifications with realistic engineering constraints.
- To design an electrical engineering project implementing an integrated design approach applying knowledge accrued in various professional courses.
- To perform simulations and incorporate appropriate adaptations using iterative synthesis.
- To use modern engineering hardware and software tools.
- To work amicably as a member of an engineering design team.
- To improve technical documentation and presentation skills.